FASTTRACK preliminary meeting

November 24th 2025

Scope of the FASTTRACK projects

- We ask for each of the program leaders (ALICE, ATLAS, LHCb, DPD and R&D) to present knowledge of their FASTTRACK projects in January

  - This is essentially an update of the information contained in the FASTTRACK proposal and in the "internal" Excel workbook attached to the indico page of the this meeting (see next slide)

- Aim to arrive at a grand overview of all FASTTRACK projects in January meeting which will allow to assess the total capacity demands.

- The final approval of the project will happen after a feasibility of the total portfolio of FASTTRACK has been assessed.

  - As you know, the procedure to approve projects and manage their lifecycles is currently discussed in a task force, which will finalize its recommendations to the DT (expected ~February next year)

Input to January meeting

We ask the program leaders, or whomever they decide to delegate this to, for an update in the "internal" Excel workbook the following information

- present your present thinking on how you would like to organize the activities in the Excel into projects

- update timeline, deliverables and milestones

  - please point out how many of these are still object of negotiations in the collaborations and eventual changes you may expect due to external pressure (international funding etc)

- updated projected baseline for technical personnel, organized as follows:

  - personpower per year (as was done in the attached Excel)

  - personpower per technical department (ET/MT/CT)

  - personpower per technical profile (see next slide)

Technical profiles

For MT:

MT.A. Simulation studies

MT.B. Studies of Thermodynamics & Heat Transfer

MT.C. Materials Science & Engineering

MT.D. Manufacturing & Production

MT.E. Design & Drafting

MT.F. Control Systems & Mechatronics

For ET:

ET.A. Analog electronics design (frontends, RF, advanced PCB's)

ET.B. Digital electronics design (DAQ)

ET.C. Analog IC design

ET.D. Digital IC design

ET.E. Technical support (cables/mechatronics/integration etc)

For CT:

CT.A. Software engineering for embedded or data acquisition

CT.B. Software engineering for control systems (SCADA)

CT.C. Software engineering for analysis frameworks / simulations

CT.D. System administration and network administration support

coordination of the FASTTRACK projects

- Once projects are approved, their lifecycle is managed through the OPP by the respective Project Leaders.

- Prior to each OPP Max (2x per year) the FASTTRACK coordinators will organize a meeting to update the FASTTRACK overview.

- WorkPackage Contacts (called *WP leads* in the FASTTRACK proposal) will act as a source of expertise that can be contacted by Project Leaders.