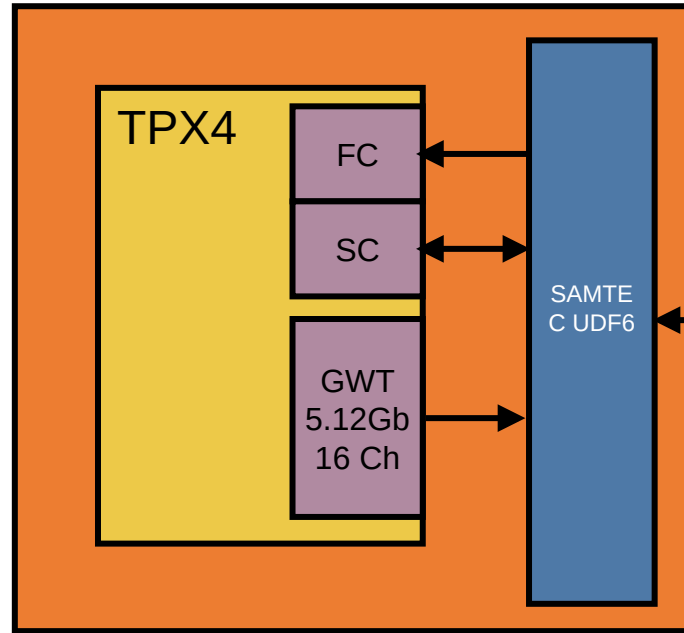
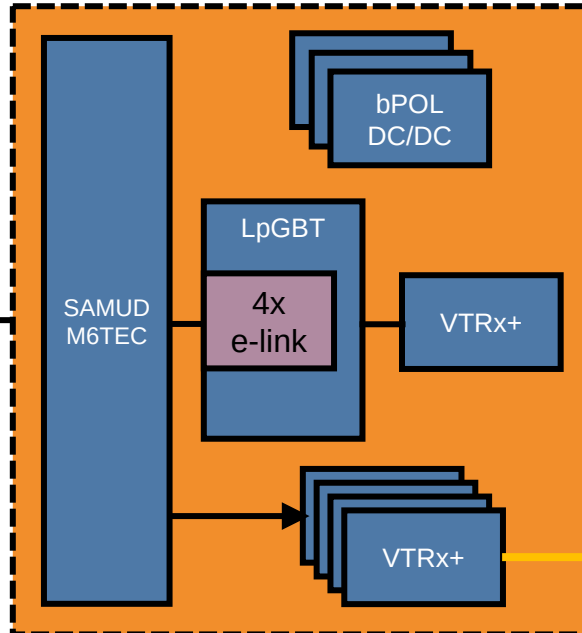


Module 0 Readout

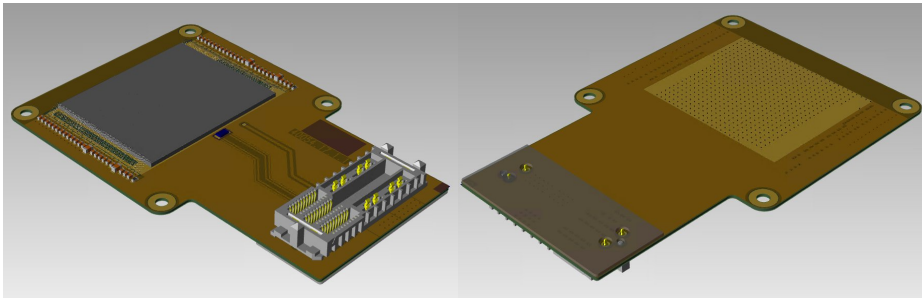
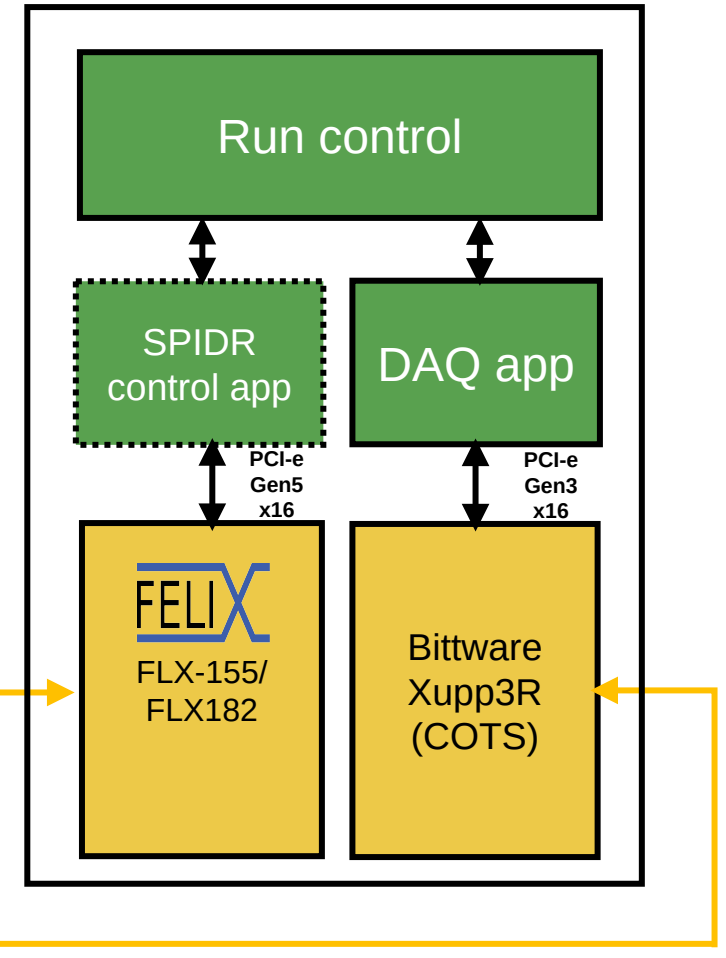
Chipboard



VLDB+ controlboard (TBD)



PC



Module 0 Readout software interface

