

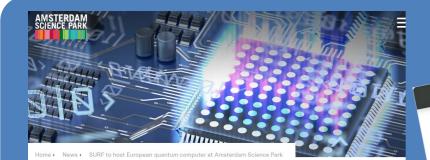
Ariana Torres Knoop

#### We are getting a quantum computer!!!



## SURF hosts European quantum computer





#### SURF to host European quantum computer at Amsterdam Science Park

Deep Tech Quantum technology and Quantum computing





#### Nieuws - 23 oktober 2024 - 10:50

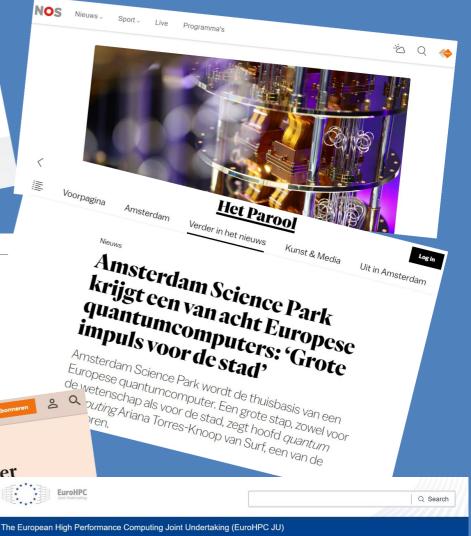
#### SURF gaat Europese kwantumcomputer in Amsterdam hosten

SURF gaat in Nederland een Europese kwantumcomputer hosten. De kwantumcomputer komt op Amsterdam Science Park te staan

Mijn nieuws Net binnen Beurs ∽ Krant Podcasts FD Persoonlijk

Grote Europese kwantumcomputer komt naar Amsterdam

Amsterdam is gekozen als locatie voor een van de kwantumcomputers die door heel Europa worde gebouwd. Brussel stimuleert de Europese ontwik



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New EuroHPC Quantum Computer to Be Hosted in the Netherlands

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PRESS RELEASE | 22 October 2024 | European High-Performance Computing Joint Undertaking | 5 min read

Access to Our

Supercomputers

New EuroHPC Quantum Computer to Be Hosted in the Netherlands

#### It takes a village...

Axel Berg Huub Stoffer Marieke Wervers

Patricia Engel-Sotomayor Wesley Kooiman Heleen Platenkamp Shalini Tijkhoeri

Dorien Brugmans-Slot Tom Hoeven ... and many more at SURF Jacco de Vries (Nikhef, UM) Joost van Echtelt Arjen van Rijn

Evert van Nieuwenbrug (Leiden, aQa) Nicolas Renaud (NLeSC) Marco Matters (TUD, Quantum Inspire Carl Mensh (VSC) Sabine Mehr (GENCI)

... and others

Thank you all for the work, input, reviewing, editing, support, discussions, etc.



"The question is not longer only about theoretical capability but also practical applicability in real computing environments" (HPCwire)



#### How do we get there?

Integration to the classical ecosystem

Applications

Implementation of quantum algorithms

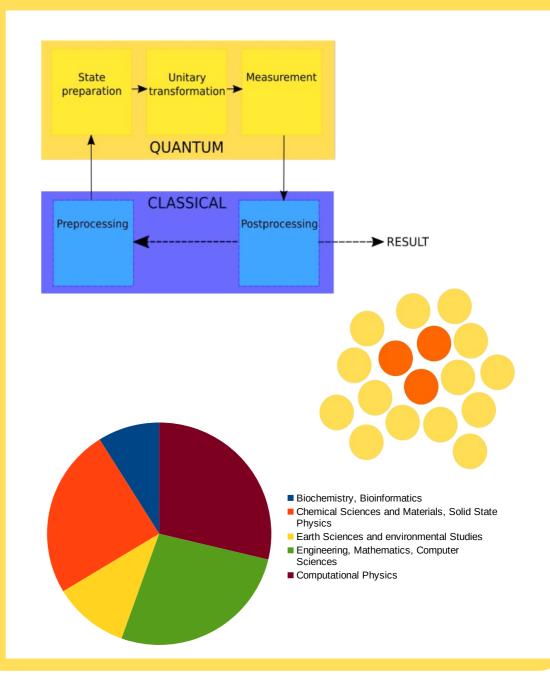
New fundamentally different algorithms (NISQ and FT)

More and more robust qubits



# Integration to classical ecosystem: HPC

- Why not?!
- Hybrid quantum algorithms are currently the only way to exploit NISQ devices
- Quantum algorithms requires the support of classical resources
- The execution of quantum algorithms will most likely always be part of a larger hybrid workflow
- Many of the most promising applications of quantum computing overlap strongly with existing applications of HPC
- Simulation of quantum computers requires large computational resources
- User base and infrastructure





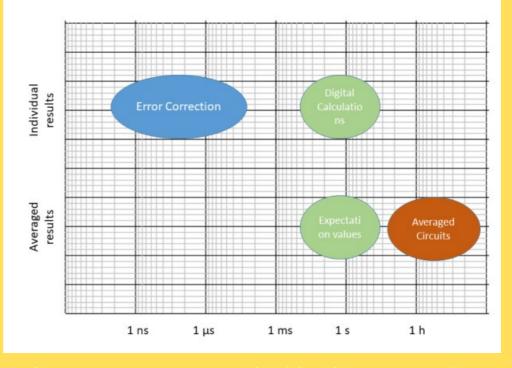
"Use of quantum computers to address existing computing challenges while leveraging the plethora of existing HPC tools, systems and workflows"

(2021 IEEE, Quantum Computers for High Performance Computing, Humble et al.)



#### **Quantum for HPC, HPC for Quantum**

- Usage of real quantum resources as accelerators: simulate quantum systems, optimizations, QML, CFD...
- Usage of classical resources to compute the theoretical output of a quantum algorithms
- Usage of classical resources for pre- post-processing (including error mitigation)
- Usage of classical resources for variational optimization
- Usage of classical resources for circuit cutting and knitting
- Usage of classical resources for error correction (conditional preparation of quantum states based on intermediate measurements)



(2021 IEEE, Quantum Computers for High Performance Computing, Humble et al.)

What is the expected resource balance? How tightly do the resources need to be coupled? What is the latency needed?



## **Quantum for HPC: what is peculiar about Quantum?**

- Tightly couple, unbalanced
- Scarce resource
- One user at the time
- Calibrations

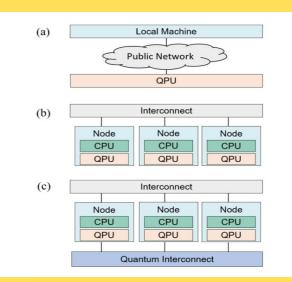
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- Non-homogeneous qubits
  - $\rightarrow$  More of a system-to-sytem integration

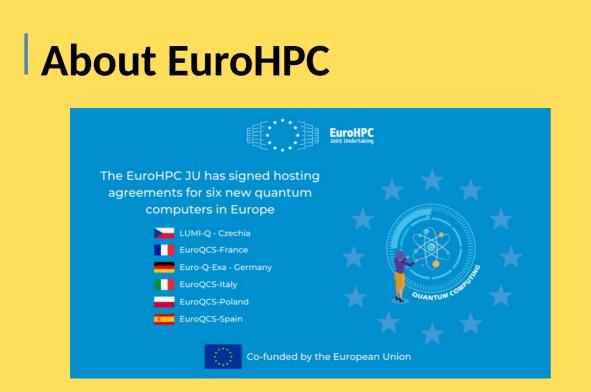
How should we schedule the jobs? What needs to be optimized? How do we share resources? Access and authentication? Resource monitoring? How should the macro and micro architecture look like?





(2021 IEEE, Quantum Computers for High Performance Computing, Humble et al.)

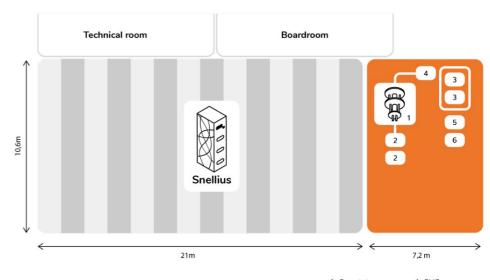




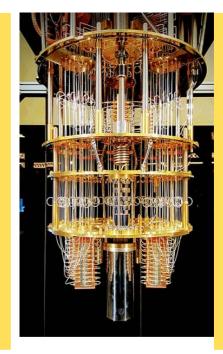
• 2 extra hosting sites

SURF

- MeluxinaQ (Lux)
- EuroSSQ-HPC (NL)



1. Cryostat4. GHS2. Control electronics5. Control unit3. Compressors6. Magnetic unit



## The consortium

University of Antwerpen (BE),

GENCI (FR),

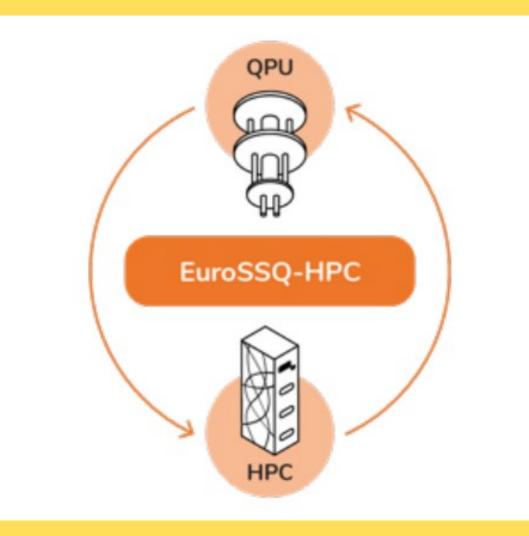
NL eScience Center (NL),

Leiden University, aQa (NL),

Delft University of Technology (NL),

National Institute of Subatomic Physics, UM (NL)

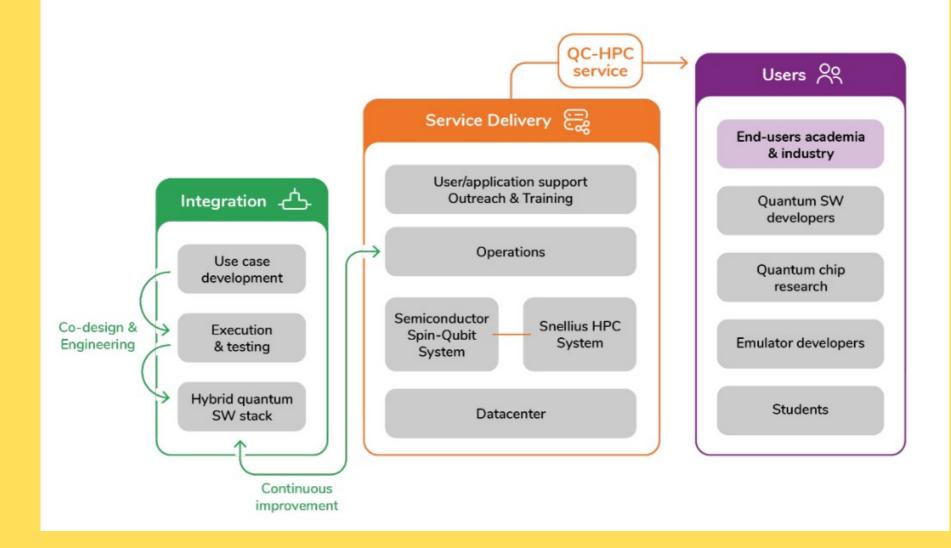
SURF (NL)





#### Our approach

SURF



## Our approach

НРС	QNode
P1	Dispatcher $\rightleftharpoons$ Quantum Device Connector $\rightleftharpoons$
	EMQNode
P1*	P1* Dispatcher → Emulated Quantum → Emulator 1
P2*	Emulator 2



### **Our quantum computer**

- Spin semiconducting qubits (Si or Si/Ge)
- Fast gates → allow for longer circuits
- Relatively long coherence times
- Small qubits  $\rightarrow$  millions in one chip
- Compatible with current semicon technology and industry
- Potentially can operate at relatively high temperature (4K)
- Greater controls due to isolation of individual electrons

https://www.youtube.com/watch?v=y9dt5uTvsEM https://www.youtube.com/watch?v=fBnKec6uI0U https://www.youtube.com/watch?v=Re4I22ycc-k https://www.youtube.com/watch?v=IPqd7A8mAzg https://www.youtube.com/watch?v=SI4gQ-kYXLI



... But the quantum computer will not be the first quantum equipment at SURF



#### SURF's quantum key distribution test bed

Provides the opportunity for researchers and developers within the Netherlands to explore innovative applications and usecases of QKD technology

#### Four end nodes:

Amsterdam SciencePark, Utrecht SURF headquarters, Groenekan Dataplace, University Medical Center Utrecht **Center hub:** Utrecht University.



