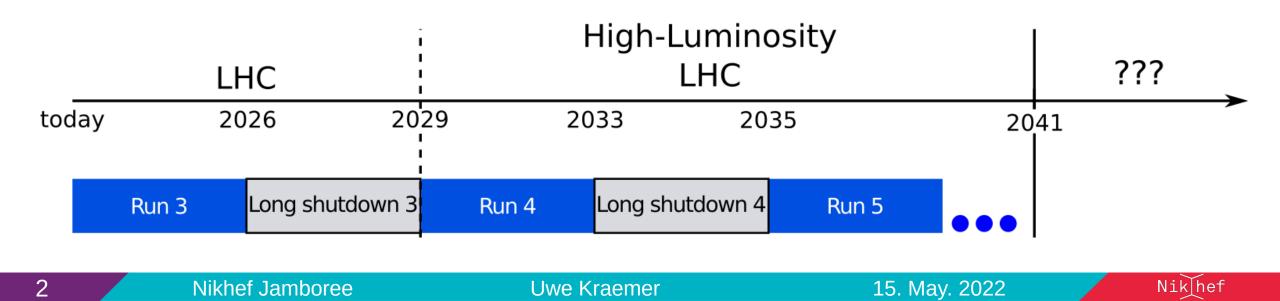


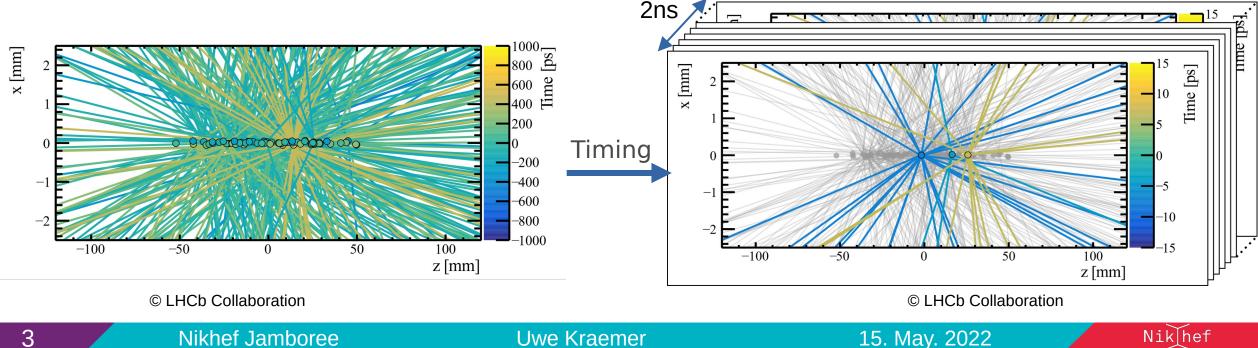
Detector R&D for the next accelerator

- Short term detector development
 - Driven by accelerator environment and physics case
- Long term detector development
 - Blue sky open development of future technologies

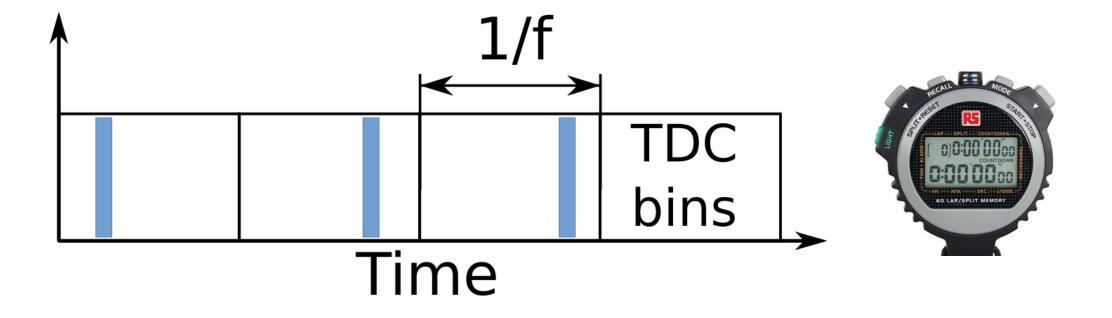


Time resolution

- Next future accelerator is the High-Luminosity upgrade of the LHC
 - More collisions per interaction window
 - Higher track densities
 - Higher amounts of radiation
- Track time resolution ~30 ps can resolve many of these issues \rightarrow 4D Tracking



$$\sigma_{t}^{2} = \sigma_{clock-global}^{2} + \sigma_{clock-on-chip}^{2} + \sigma_{TDC}^{2} + \dots$$

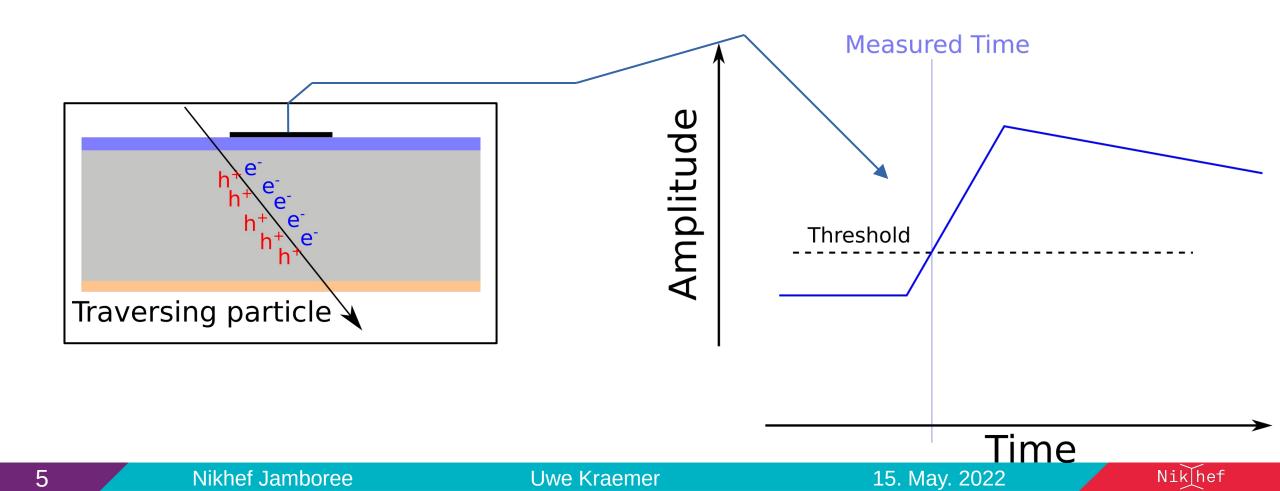


4

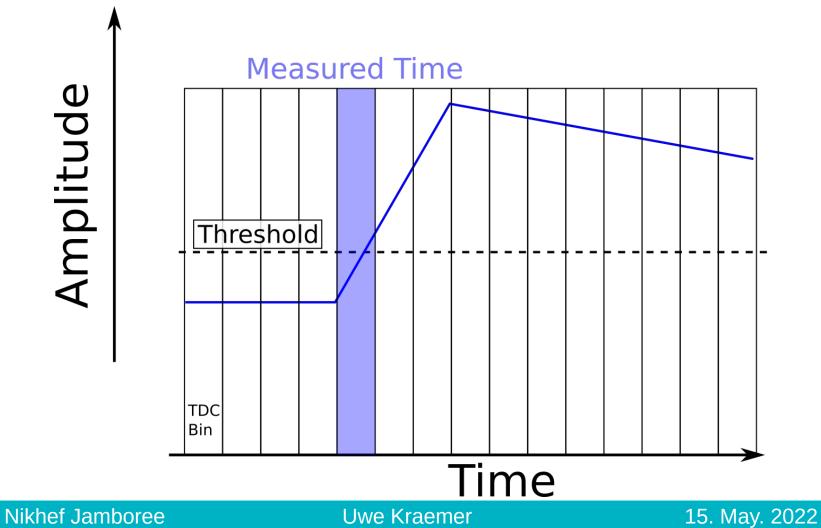


What is actually measured

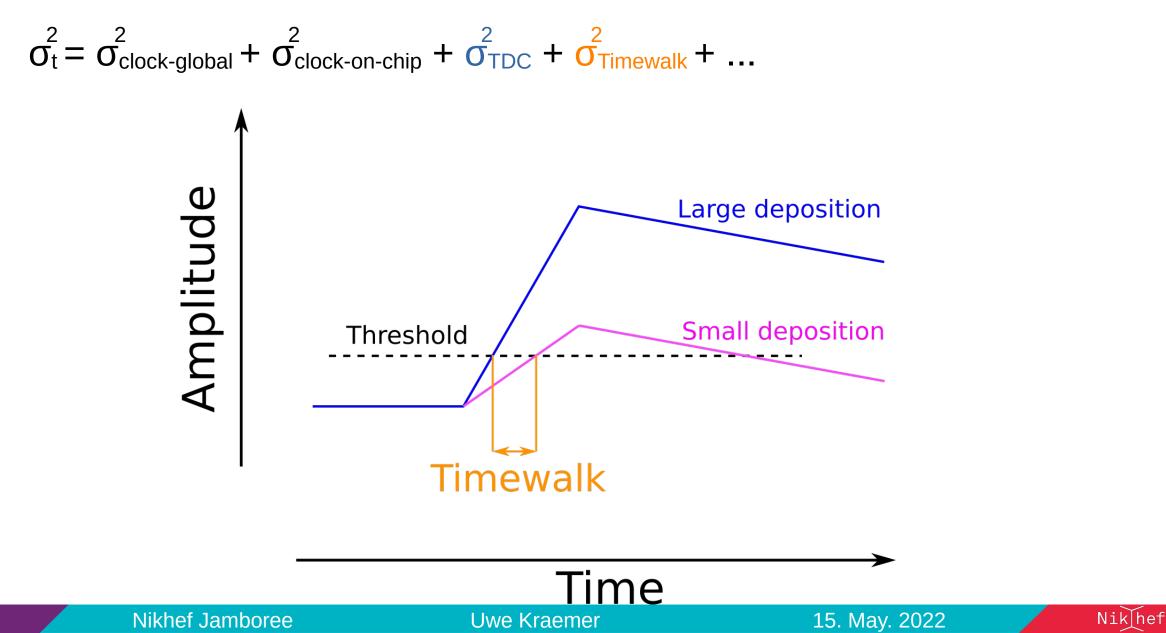
• Measured is the time when a gathered charge signal crosses a threshold



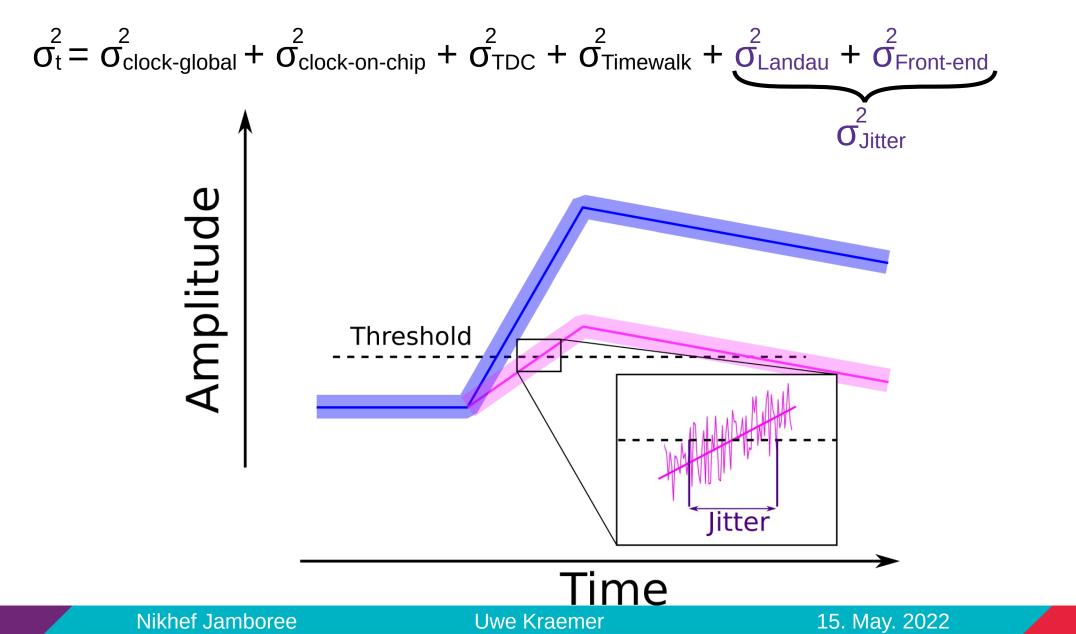




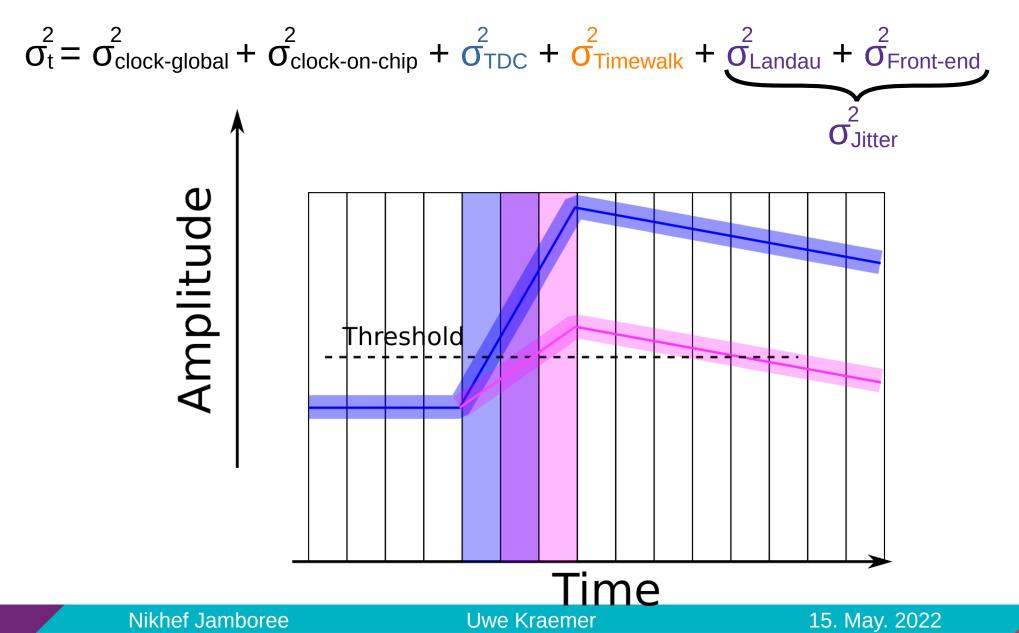




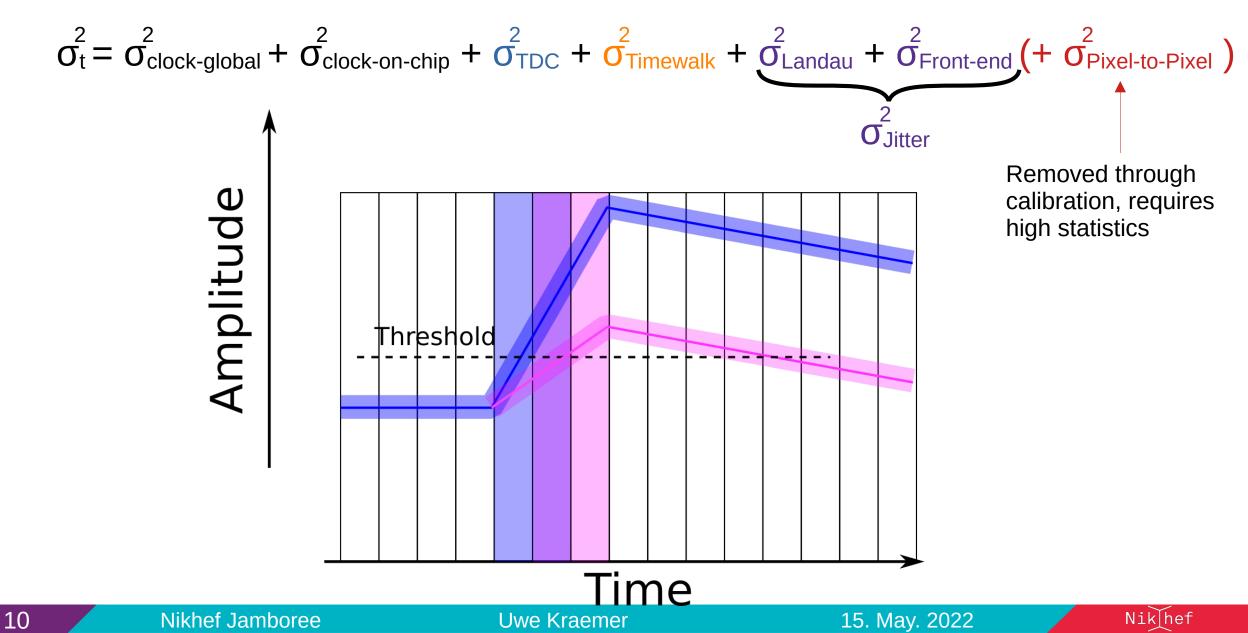
8



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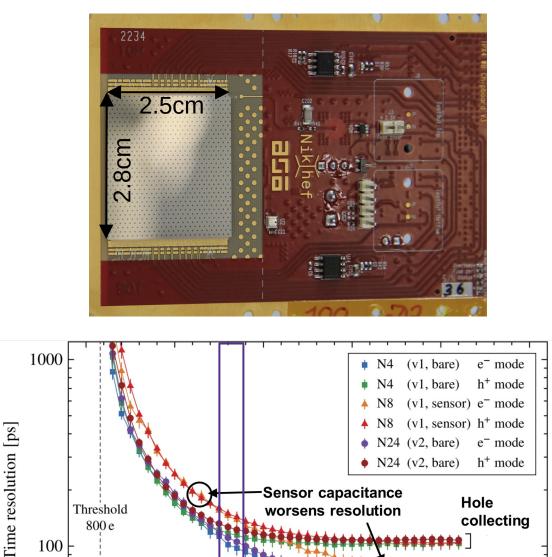




Moving beyond digital limits

- Older ASICs
 - Timepix3 $\sigma_{\text{TDC}} \sim 450 \text{ ps}$
- Newer ASICs
 - Timepix4 $\sigma_{TDC} \sim 62 \text{ ps}$
- Next goal:
 - Picopix σ_{TDC} < 20 ps

- Impact of other contributions begin to be significant
 - Only capacitive load from sensor
 σ_{Front-end} ~ 100 ps _____



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Injected charge [ke]

15

TDC resolution

56-62 ps

40



Electron

20

collecting

25

Uwe Kraemer

Moving beyond digital limits

• Single pixel measurement with charge induced in the sensor via TPA laser

•
$$\sigma_t^2 = \sigma_{clock-global}^2 + \sigma_{clock-on-chip}^2 + \sigma_{TDC}^2 + \sigma_{Front-end}^2$$

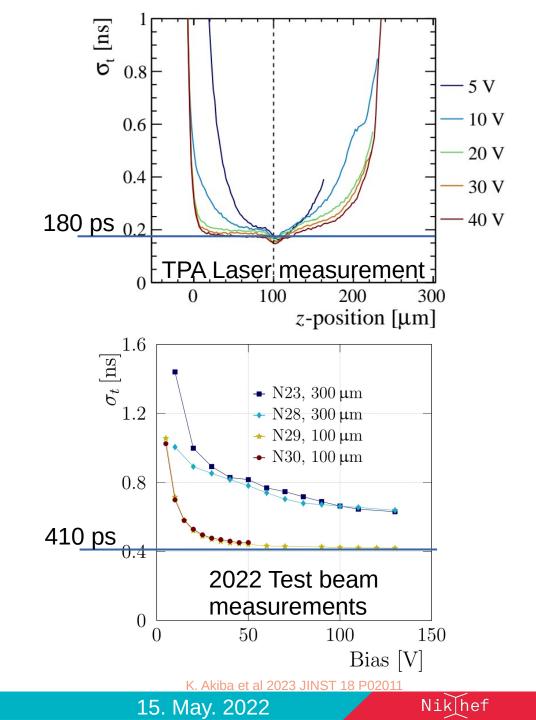
• $\sigma_t = \sim 180 \text{ ps}$

• Full chip measurement via test beam

•
$$\sigma_t^2 = \sigma_{clock-global}^2 + \sigma_{clock-on-chip}^2 + \sigma_{TDC}^2 + \sigma_{Timewalk}^2 + \sigma_{Landau}^2 + \sigma_{Front-end}^2 + \sigma_{Pixel-to-Pixel}^2$$

• $\sigma_t = \sim 410 \text{ ps}$

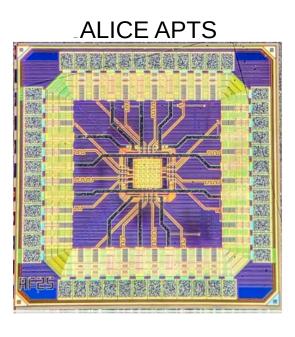
• Sensor type and structure start to play a major role

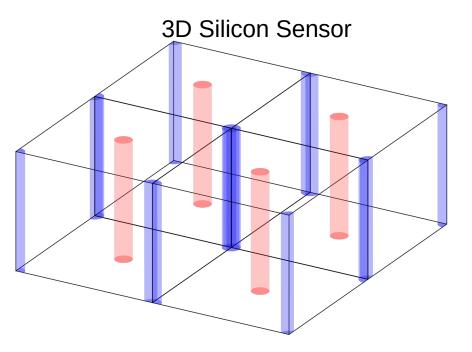


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Beyond digital limits

- Different sensor structures under investigation
 - Monolithic Active Pixel Sensors (MAPS) for their fast charge collection and low capacitance
 - Low Gain Avalanche Diodes (LGAD) for their internal charge amplification
 - 3D sensors for their large signal generation and fast charge collection



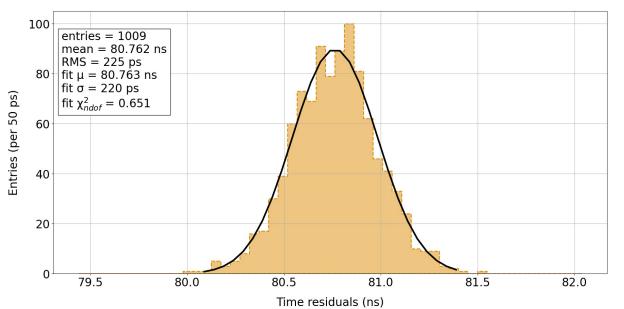


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ALICE MAPS

- Analog Pixel Test Structure (APTS) produced in different design structures
 - First tested standard process
 - σ_{Front-end} ~ 220 ps with wide laser focused on pixel center



- Modified process
 - $\sigma_{\text{Front-end}}$ < 80 ps in whole pixel

pwell collection electrode deep pwell dep pwel

Fig.: Standard ALICE MAPS

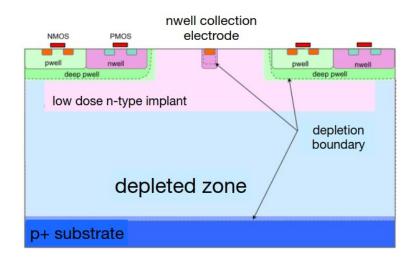


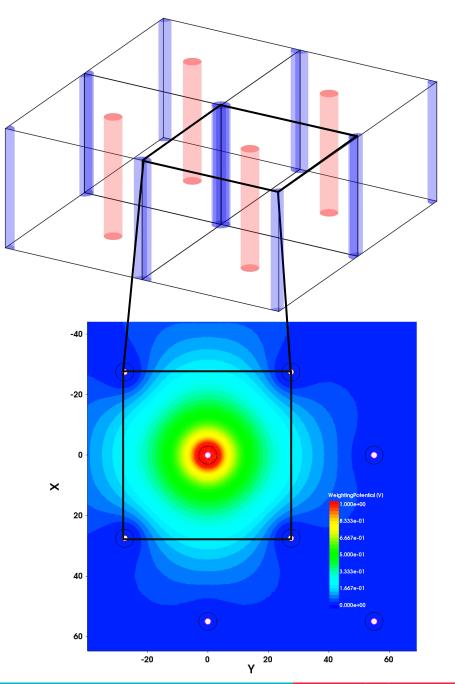
Fig.: Modified ALICE MAPS

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3D Sensors and Simulations

- 3D sensors:
 - Can be made thick as distance to electrodes doesn't depend on thickness
 - Fast charge collection
 - Large signal
 - More complex behavior with respect to track angle, charge sharing etc.
- Simulations allow for:
 - The modeling of complex structure
 - Investigation into single parameters to the overall time resolution
 - Verification laboratory and testbeam measurement



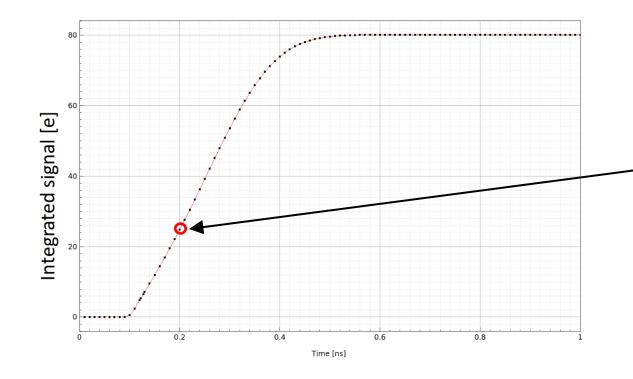
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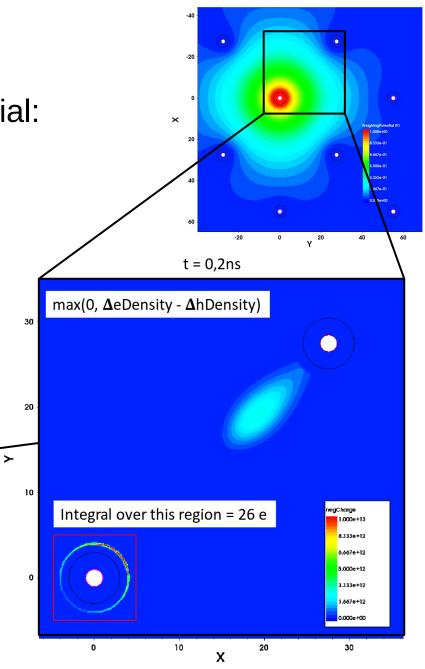
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3D Sensors and Simulations

- Simulations of the sensor structures are essential:
 - Understanding system behavior
 - Verifying measurement results
 - Investigating potential design criteria

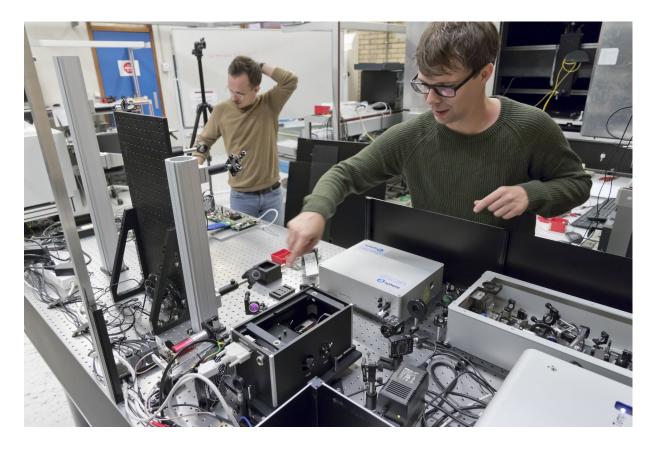




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A tale of photon- and charged beams

Laser setups at the Nikhef Laboratories



Test beam measurements at CERN and DESY



Uwe Kraemer



Summary and Outlook

- Time resolution becoming essential for future collider experiments
- Time resolution is affected by both digital and analog components that all have interplay

- The Detector R&D group is working at the forefront of the fast timing efforts looking into many possible candidates for future technologies
- With the accepted FASTER proposal the efforts at Nikhef to produce detectors with the best possible time resolution will only be improved further



Backup slides

Nikhef

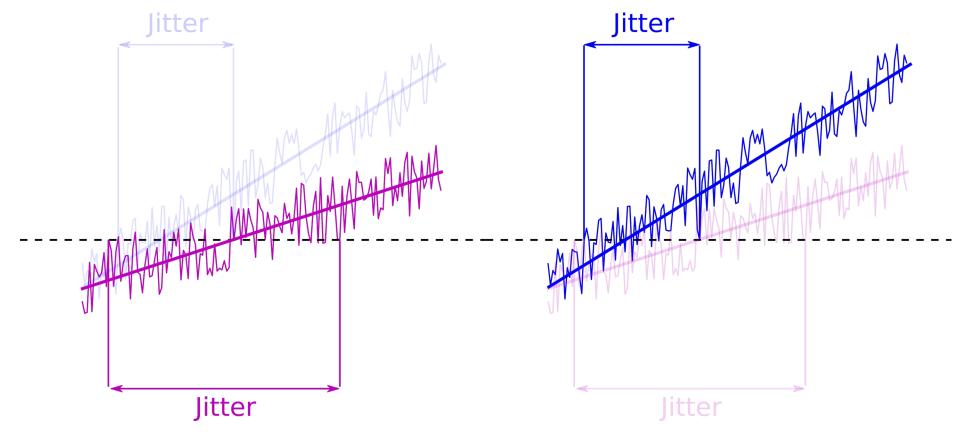
Nikhef Jamboree

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Uwe Kraemer

Signal rise time

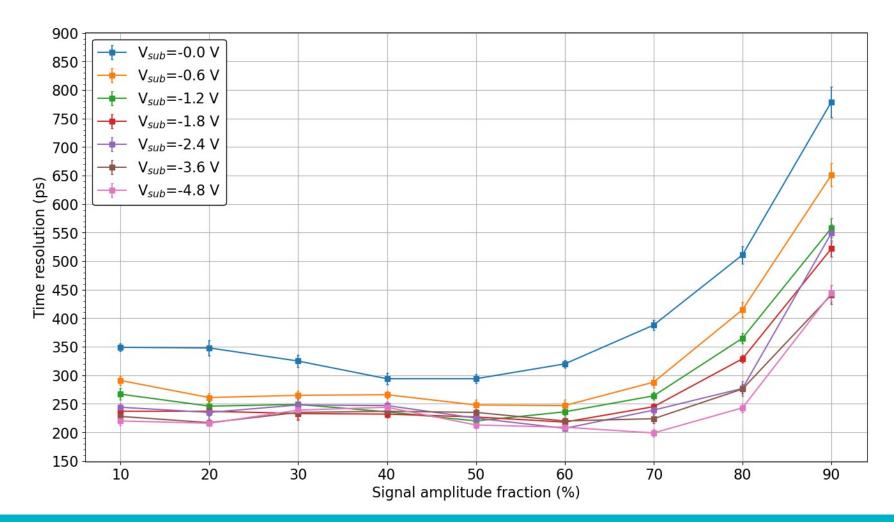
- Jitter depends on front-end noise and therefore capacitance
- For the same amount of noise a fast rising signal is impacted less for its time resolution





ALICE MAPS cont.

Investigation of APTS structure concerning different settings



Nikhef Jamboree